Design and Realization of Band Pass Filter in K-Band Frequency for Short Range Radar Application

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Abstract

Short range radar (SRR) uses the K-band frequency range in its application. The radar requires high-resolution, so the applied frequency is 1 GHz wide. The filter is one of the devices used to ensure only a predetermined frequency is received by the radar system. This device must have a wide operating bandwidth to meet the specification of the radar. In this paper, a band pass filter (BPF) is proposed. It is designed and fabricated on RO4003C substrate using the substrate integrated waveguide (SIW) technique, results in a wide bandwidth at the K-band frequency that centered at 24 GHz. Besides the bandwidth analysis, the analysis of the insertion loss, the return loss, and the dimension are also reported. The simulated results of the bandpass filter are: VSWR of 1.0308, a return loss of -36.9344 dB, and an insertion loss of -0.6695 dB. The measurement results show that the design obtains a VSWR of 2.067, a return loss of -8.136 dB, and an insertion loss of -4.316 dB. While, it is obtained that the bandwidth is reduced by about 50% compared with the simulation. The result differences between simulation and measurement are mainly due to the imperfect fabrication process.

Keywords: Short range radar, band pass filter, microstrip, substrate integrated waveguide, K-band.

I. INTRODUCTION

In the original K-band, there is a water vapor absorption line at 22.2 GHz which causes a serious problem of attenuation in some applications. The radar echo from rain can limit the capability of radars at these frequencies[1]. However, K-band frequency range is still attractive for a small size radar that is used for an application that does not require long-range detection. Several fields such as automotive industry utilize short range radar (SRR) for road safety and intelligent transport systems [1]-[3].

Automotive short range radar (SRR) provides various functions to increase drivers’ safety and convenience. For SRR to have sufficiently high range resolution for detecting small objects at close range, it requires a wide bandwidth. The availability of wide bandwidth significantly improves range resolution which results in better separation of objects. With higher range resolution, it improves an SRR system’s minimum distance of detection.

There are several devices that are part of the SRR system, one of which is the filter. A filter with certain specifications is needed so that it can be used in SRR systems. To have a filter designed with a low insertion loss, small size, and limited cost is essential for the manufacture of microwave systems. Unfortunately, the traditional technology, either planar or non-planar, is incapable to provide all these characteristics at the same time. In fact, the rectangular waveguides present low insertion losses and good selectivity [4]. Recently, a substrate integrated waveguide (SIW) is one popular method to design microstrip filters. This method is similar to the conventional rectangular waveguide, and can be used in the K-band frequency range in order to meet the bandwidth and filter characteristics requirement, including insertion loss, return loss, and minimization of filter dimensions. Figure 1 shows the similarity of the electric field distribution in the SIW and equivalent conventional waveguide. In the SIW filter structure, the via sequences are connected between the top and bottom of the conductor forming a cavity wall, where the microstrip transmission line is used for the feeding of the RF input and output.

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The cavity wall consists of via rows that provide out-of-band rejection, making it perfect to be used in radar applications [6]. Basically, the microstrip transmission line is also functioned to guide a signal that enters a device. However, the thing that makes SIW different from other microstrips is the presence of the two rows of metal cylinders, causing low-frequency signals to not propagate in the SIW [7]. Also, a wide operating bandwidth can be achieved using the SIW, as reported in [8]-[9]. In [8], a hybrid and periodically drilled SIW (PDSIW) structure are proposed. While in [9], a substrate integrated plasmonic waveguide (SIPW) concept is used to obtain a wide bandwidth.

In this paper, a design of wideband bandpass filter (BPF) using the SIW technique is proposed. The device is designed and fabricated on RO4003C substrate. This BPF will operate at the K-band with a center frequency of 24 GHz.

II. Filter Design and Simulation

A. Design Methodology

The proposed design of bandpass filter is intended to pass signals at the center frequency of 24 GHz with a bandwidth of 1 GHz. The bandpass filter is designed as a part of the SRR block system. The flow chart of the filter design stages is depicted in Figure 2. It is started by determining the filter specification and dimension, followed by simulation and realization. Filter performances must meet the value of certain parameters such as insertion loss, return loss, and VSWR.

![Flowchart of Filter Design](image)

The filter specifications are as follows:

- Frequency Range: 23.5 GHz – 24.5 GHz
- Center Frequency: 24 GHz
- Bandwidth: 1 GHz
- Return Loss: ≤ -10 dB
- Insertion Loss: ≥ -3 dB
- VSWR: ≤ 2
- Frequency Response: Chebyshev
- Substrate: Rogers RO4003C (Thickness: 0.813 mm) (Dielectric Constant (Ɛr): 3.55)

After determining the specifications, next step is to calculate the dimensions of the filter. This process includes determining the order, the diameter, and the distance between the vias using a predetermined formula. Afterward, the simulation is conducted to obtain S-parameter values of the designed filter with calculated dimensions in the previous stage. If the simulation results are not in accordance with the specifications, optimization will be carried out, but if the simulation results have met the requirements, the filter design will be fabricated. The filter that has been fabricated will then be measured and compared with the simulation results. Then, the difference between the two results will be analyzed.

B. Substrate Integrated Waveguide (SIW)

The characteristics of SIW are similar to that of a rectangular waveguide. The combination of a non-planar circuit (waveguide) and a planar shape (microstrip) offers various advantages, namely the characteristics of a non-planar waveguide can be obtained from SIW structures such as low insertion loss, low radiation loss, small EM interference, and low-cost fabrication.

The thin dielectric substrate on SIW does not allow transverse magnetic (TM) mode to resonate. Therefore, in SIW only the transverse electric (TE) mode can transmit effectively [10], [11]. In Figure 3 there are several important parameters to form the via wall. To find the diameter of the via, the distance between the via series, and the distance between the rows, following equation is used [12].

\[
\frac{d}{\lambda} < \frac{5}{3} 
\]  

\[
p \leq 2d 
\]  

\[
a = \omega - \frac{d^2}{0.95 \times p} 
\]

![Via Structure on Substrate Integrated Waveguide](image)
SIW filter can be designed using additional set of vias that is placed between the via walls. Figure 4 shows the parameters of the diameter and distance between the vias. To find these parameters, the following equation is used:

\[
\omega = \frac{\omega_2 - \omega_1}{\omega_0} \quad \text{(4)}
\]

\[
\omega \lambda = \frac{\lambda G}{\lambda_0} \quad \text{ω} \quad \text{(5)}
\]

after that, substitute the values in equations (4) and (5) as the parameters in the Chebyshev frequency response, in equations (6) and (7) [14].

\[
\frac{K_01}{Z_0} = \frac{K_{34}}{Z_0} = \frac{\pi \times \omega \lambda}{2 \times \sqrt{g_0 g_1} \omega_1} \quad \text{(6)}
\]

\[
\frac{K_{23}}{Z_0} = \frac{\pi \omega \lambda}{2 \omega_1} \times \frac{1}{\sqrt{g_1 g_2}} \quad \text{(7)}
\]

Then substitute equations (6) and (7) into equation (8). This equation calculates the final impedance for each stage in equations (6) and (7) [14].

\[
\frac{X_{j,i}}{Z_0} = \frac{\frac{K_{j,i+1}}{Z_0}}{1 - \left(\frac{K_{j,i+1}}{Z_0}\right)^2} \quad \text{(8)}
\]

The dimensions of the via filter can be determined using the following equation [14]:

\[
\theta_j = \pi - \frac{1}{2} \tan^{-1} \left(2 \times \frac{X_{j-1,i}}{Z_0}\right) + \tan^{-1} \left(2 \times \frac{X_{j,i+1}}{Z_0}\right) \quad \text{(9)}
\]

\[
l_1 = l_3 = \frac{\theta_1 \times \lambda_g}{2 \pi}, l_2 = \frac{\theta_2 \times \lambda_g}{2 \pi} \quad \text{(10)}
\]

To match the impedance between the feed and the SIW components, a taper is introduced to connect these two. The dimensions of the taper can be determined using equations (13) and (14) [13].

\[
f_{CTE20_{slow}} = \frac{c}{\sqrt{\varepsilon r}} \left(a - \frac{d^2}{1.1p} + 0.1 \frac{d^2}{6.6p} \right)^{-1} \quad \text{(12)}
\]

\[
l f_2 = \frac{2c}{3(1.25 \times f_{CTE20_{slow}} + 0.95 \times f_{CTE20_{slow}}) \sqrt{\varepsilon r}} \quad \text{(13)}
\]

\[
w f_2 = \frac{c}{3(1.25 \times f_{CTE20_{slow}} + 0.95 \times f_{CTE20_{slow}}) \sqrt{\varepsilon r}} \quad \text{(14)}
\]

Before determining the dimensions of the filter, the first thing to consider is the order of the filter to be used. Based on the specifications, the most suitable frequency response is Chebyshev, with a ripple of 0.1 dB. To determine the filter order (n), following equations are used:

\[
FWB = \frac{\omega_2 - \omega_1}{\omega_0} \quad \text{(15)}
\]

\[
\Omega_s = \frac{2}{FWB} \left(\frac{\omega_2 - \omega_1}{\omega_0}\right) \quad \text{(16)}
\]

\[
n \geq \frac{\cosh^{-1} \left(\frac{1}{\omega_1 \pi} \cosh^{-1} \frac{1}{\Omega_s} - 1\right)}{\cosh^{-1} \Omega_s} \quad \text{(17)}
\]

The next step is to calculate the diameter and distance between the via walls, then determine the diameter and distance between the filter vias. After that, the ratio between the length and width of the tapper is calculated. Finally, a 3-pole Chebyshev SIW filter based on the equation is shown in Figure 5 and the detailed parameter values are shown in Table 1.

The simulation results of the filter performance at the center frequency of 24 GHz show the insertion loss of -4.0437 dB, return loss -4.0820 dB, VSWR of 4.337, and bandwidth of 256 MHz. These results did not meet the initial specifications. Therefore, an optimization step is carried out. Optimization by adding vias was carried out to obtain nearly ideal $S_{11}$ & $S_{22}$ parameters. Vias were added to guide the incoming waves from the feeder and tapper through the area with the via filter, and thus minimize the unwanted scattered waves.

In addition, vias located at the very end of both sides of the filter close to the tapper and feeder, are shifted by 0.8 mm to adjust the shape of the feeder and tapper. The purpose is to guide the incoming waves directly. The optimized design results are shown in Figure 6 and the dimensional changes are shown in Table 3.
Table 1. Dimensions of the Filter

<table>
<thead>
<tr>
<th>No.</th>
<th>Parameter</th>
<th>Value (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>d</td>
<td>0.6</td>
</tr>
<tr>
<td>2</td>
<td>p</td>
<td>1.2</td>
</tr>
<tr>
<td>3</td>
<td>a</td>
<td>3.0014</td>
</tr>
<tr>
<td>4</td>
<td>d1</td>
<td>0.6</td>
</tr>
<tr>
<td>5</td>
<td>d2</td>
<td>0.6</td>
</tr>
<tr>
<td>6</td>
<td>l1 &amp; l3</td>
<td>1.6449</td>
</tr>
<tr>
<td>7</td>
<td>l2</td>
<td>3.65</td>
</tr>
<tr>
<td>8</td>
<td>l1f1</td>
<td>1.824930</td>
</tr>
<tr>
<td>9</td>
<td>w1f1</td>
<td>2.043870</td>
</tr>
<tr>
<td>10</td>
<td>l2f1</td>
<td>0.688445232</td>
</tr>
<tr>
<td>11</td>
<td>w1f2</td>
<td>0.688445232</td>
</tr>
</tbody>
</table>

Figure 7 depicts the result of optimization where the S21 parameter or insertion loss value has a slight change but still meets the requirement value. At the 23.5 GHz frequency, which previously -0.7120 dB, the insertion loss decreases to -1.1086 dB, at the 24.5 GHz frequency, which previously -1.2473 dB, the S21 decreases to -1.2689 dB, while at the middle frequency which previously received S21 of -0.9124 dB, is now changes to -0.6695 dB.

Table 2. Optimization Parameter

<table>
<thead>
<tr>
<th>No.</th>
<th>Parameter</th>
<th>Value (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>d</td>
<td>0.6</td>
</tr>
<tr>
<td>2</td>
<td>p</td>
<td>1.2</td>
</tr>
<tr>
<td>3</td>
<td>a</td>
<td>2.4</td>
</tr>
<tr>
<td>4</td>
<td>d1</td>
<td>0.6</td>
</tr>
<tr>
<td>5</td>
<td>d2</td>
<td>0.8</td>
</tr>
<tr>
<td>6</td>
<td>l1 &amp; l3</td>
<td>3.9473021532016</td>
</tr>
<tr>
<td>7</td>
<td>l2</td>
<td>3.5378823279004</td>
</tr>
<tr>
<td>8</td>
<td>l1f1</td>
<td>10.039874138099</td>
</tr>
<tr>
<td>9</td>
<td>w1f1</td>
<td>0.37783444116295</td>
</tr>
<tr>
<td>10</td>
<td>l2f1</td>
<td>1.2341273596467</td>
</tr>
<tr>
<td>11</td>
<td>w1f2</td>
<td>5</td>
</tr>
<tr>
<td>12</td>
<td>offsd1</td>
<td>0.48</td>
</tr>
<tr>
<td>13</td>
<td>offsd2</td>
<td>0.6</td>
</tr>
</tbody>
</table>

Figure 8 shows the VSWR value. There is a slight change but still meets the specification value. The VSWR at 23.5 GHz frequency was previously 1.3319, now is 1.9209, at the 24.5 GHz frequency, of the VSWR was previously 1.3602, now is 1.2283, while the VSWR at the middle frequency changes from 1.6028 to 1.0308. The lower stopband area, which was previously at 23.4190 GHz, has shifted to 23.4870 GHz. Meanwhile, the upper stopband, which was previously at 24.64 GHz, has shifted to 24.6060 GHz.

III. Fabrication, Measurement, and Discussion

A. Fabrication

The proposed SIW filter is fabricated on RO4003C substrate with 0.813 mm thickness, as shown in Figure 9. The fabricated bandpass filter is measured using a microwave analyzer measuring instrument N9918A which has a measurement frequency range from 30 KHz to 26.5 GHz. The measurement is carried out to find out the technical data from the proposed filter design, namely insertion loss, return loss, and VSWR. The measurement results data then will be compared with the design specification data.
Figure 9. Fabricated Filter (a) Top View (b) Bottom View.

B. Measurement

Before starting a measurement, a calibration is carried out using a calibrator shown in Figure 10. In this step, it is found that there is a loss of -0.8109 dB that will affect the measurement results. This is marked with a red box in Figure 10.

C. Results and Discussion

Figure 11 shows the measurement results of the $S_{21}$ or insertion loss parameter. Although there is a loss of -0.8109 dB from the measurement system, the device’s performances still meet the pre-determined specification at 23.5 to 24.5 GHz. Considering the system loss, the $S_{21}$ at 23.5 GHz frequency is -3.338 dB and at 24.5 GHz is -4.814 dB, while at 24 GHz is -4.316 dB. It is seen in the graph, there is a change in the measured bandwidth.

Figure 12 shows the measurement results of the $S_{11}$ parameter or return loss. In the frequency range of 23.5 to 24.5 GHz, the value obtained still meets the specification target when the result has been reduced by the loss of -0.8109 dB which was known from the previous calibration.

If the system loss is taken into account, the value of $S_{11}$ at the 23.5 GHz frequency is -10.139 dB, at 24.5 GHz is -10.969 dB while at the 24 GHz frequency is -8.136 dB. Similar to the previous $S_{21}$ measurement, there is a shift in the $S_{11}$ of the bandpass section and it appears that the bandwidth is decreasing compared to the simulation.

Figure 13 shows a graph of VSWR measurement. The measured VSWR are 1.833 at 23.5 GHz, 1.649 at 24.5 GHz, and 2.067 at 24 GHz. Based on the overall measurement, all of the results at the frequency range of 23.5 to 24.5 GHz still meet the requirements that are specified earlier. It is noted, however, that the measurement results are far from ideal. The measured operation frequency is slightly shifted and the bandwidth is reduced compared to the simulation results.

Based on the filter performance, although the insertion loss, return loss, and VSWR parameters still meet the initial specifications, there is a significant difference of the measured bandwidth parameter compared to the simulation. The summary of the performance results is shown in Table 3.
The bandwidth discrepancy that occur during the measurement can be ascertained due to the fabrication factors. Based on the formula used when performing calculations, the diameter of the via filter and the distance between the via filters have an influence on the generated bandwidth. In addition, the dimensions of the tapper also have a significant effect on the bandwidth and the value of other parameters, because this section is an adjustment between the feeder and the components of SIW.

Furthermore, the imperfect drilling process on the substrate in the SIW method also contributes to the result discrepancies. If the drilling process is done manually, there will be a possibility of size or placing inaccuracy that may affect the measured device’s performances. In other words, a high precision fabrication is needed in the prototyping of the bandpass filter using the SIW method. In addition, fabrication losses may arise due to poor soldering of the connectors that can increase the attenuation of the filter.

**CONCLUSION**

We have designed and realized a bandpass filter using a substrate integrated waveguide structure. The design of the bandpass filter has a good agreement with the specification of short-range radar applications with a working frequency of 24 GHz. The realization of the bandpass filter gives good performances in terms of insertion loss, return loss and VSWR. Nevertheless, the bandwidth is reduced by about 50% compared with the simulation bandwidth. It is caused by the fabrication process such as hole drilling that requires precision. Furthermore, the change of the tapper dimension during the fabrication also influence the bandwidth.

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